



2-21-02#5  
Patent  
Docket No. 50544.00004 (NVLS 379)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Craig L. Stevens, et al.

Serial No. 09/872,796

Filed: June 1, 2001

For: ARCHITECTURE FOR HIGH  
THROUGHPUT SEMICONDUCTOR  
PROCESSING APPLICATIONS

Examiner: Unknown

Art Unit: 2812

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Commissioner for Patents  
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

The information listed below may be material to the examination of the above-identified application. Copies of the cited reference(s) and a completed PTO-1449 form are submitted herewith. The Examiner is requested to make this information of official record in the application.

Applicants would appreciate the Examiner's initialing and returning the Form PTO-1449, indicating that the reference(s) have been considered and made of record herein.

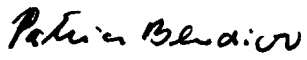
The Information Disclosure Statement under 37 CFR §1.97 is not to be construed as a representation that: (i) a search has been made; (ii) additional information material to the examination of this application does not exist; (iii) the information, protocols, results and the like reported by third parties are accurate or enabling; or (iv) the above information constitutes prior art to the subject invention.

If for any reason an insufficient fee has been paid, the Commissioner is hereby authorized to charge any deficiency in payment of required fees associated with this communication to Deposit Account **05-0150**.

Dated: August 24, 2001

Respectfully submitted,

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